

Appl. No. 09/822,684
Amdt. Dated 1/30/2006
Reply to Final Office Action of 01/09/2006

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (canceled) A method comprising:
configuring a mode word in a chipset or in a configuration map stored in a non-volatile memory during boot-up;
detecting insertion of a medium into a drive based on the mode word; and
starting a program on the medium when the insertion is detected.
2. (canceled) The method of claim 1 wherein configuring the mode word comprising:
configuring the mode word in one of first, second, third, and fourth modes.
3. (currently amended) The method of claim [[2]] 6 wherein detecting the insertion comprises:
periodically polling the drive when the mode word is configured in the first mode.
4. (canceled) The method of claim 2 wherein detecting the insertion comprises:
servicing an interrupt indicating the insertion of the medium when the mode is configured in one of the second, third, and fourth modes.
5. (canceled) The method of claim 4 wherein servicing the interrupt comprises:
servicing the interrupt generated by a polling circuit in the chipset when the mode is configured in one of the second and third modes, the polling circuit detecting the insertion of the medium.
6. (currently amended) ~~The A method of claim 5 comprising:~~

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configuring a mode word in one of first, second, third, and fourth modes in a chipset or in a configuration map stored in a non-volatile memory during boot-up;
detecting insertion of a medium into a drive based on the mode word, detecting the insertion comprising servicing an interrupt indicating the insertion of the medium when the mode is configured in one of the second, third, and fourth modes, wherein servicing the interrupt comprises servicing the interrupt generated by a polling circuit in the chipset when the mode is configured in one of the second and third modes, the polling circuit detecting the insertion of the medium; and

starting a program on the medium when the insertion is detected;

wherein servicing the interrupt comprises:

checking a status bit set by the polling circuit when the mode is configured in the second mode;

updating a flag in a memory based on the status bit; and

responding to a poll request by an operating system.

7. (original) The method of claim 6 wherein responding comprises:
reading the flag from the memory.
8. (currently amended) The method of claim [[4]] 6 wherein servicing the interrupt comprises:
servicing the interrupt generated by the drive.
9. (withdrawn) A method comprises:
checking a status bit in response to an interrupt generated by a polling circuit in a chipset, the polling circuit detecting insertion of a medium into a drive;
updating a flag in a memory based on the status bit; and
responding to a poll request by an operating system.
10. (withdrawn) The method of claim 9 wherein responding comprises:
reading the flag from the memory.

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11. (withdrawn) The method of claim 9 wherein checking the status comprises:
checking the status bit set by the polling circuit upon detecting the insertion of the medium.
12. (withdrawn) The method of claim 9 further comprises:
loading a program on the medium into a memory; and
executing the program.
13. (canceled) A computer program product comprising:
a machine useable medium having computer program code embedded therein, the computer program product having:
computer readable program code to configure a mode word in a chipset or in a configuration map stored in a non-volatile memory during boot-up;
computer readable program code to detect insertion of a medium into a drive based on the mode word; and
computer readable program code to start a program on the medium when the insertion is detected.
14. (canceled) The computer program product of claim 13 wherein the computer readable program code to configure the mode word comprising:
computer readable program code to configure the mode word in one of first, second, third, and fourth modes.
15. (currently amended) The computer program product of claim ~~14~~ 18 wherein the computer readable program code to detect the insertion comprises:
computer readable program code to periodically poll the drive when the mode word is configured in the first mode.
16. (canceled) The computer program product of claim 14 wherein the computer readable program code to detect the insertion comprises:

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computer readable program code to service an interrupt indicating the insertion of the medium when the mode is configured in one of the second, third, and fourth modes.

17. (canceled) The computer program product of claim 14 wherein the computer readable program code to service the interrupt comprises:

computer readable program code to service the interrupt generated by a polling circuit in the chipset when the mode is configured in one of the second and third modes, the polling circuit detecting the insertion of the medium.

18. (currently amended) ~~The A~~ computer program product ~~of claim 17~~ comprising:
a machine useable medium having computer program code embedded therein, the
computer program product having:

computer readable program code to configure a mode word in one of first,
second, third, and fourth modes in a chipset or in a configuration map stored in a
non-volatile memory during boot-up;

computer readable program code to detect insertion of a medium into a
drive based on the mode word, wherein the computer readable program code to
detect the insertion comprises computer readable program code to service an
interrupt indicating the insertion of the medium when the mode is configured in
one of the second, third, and fourth modes, wherein the computer readable
program code to service the interrupt comprises computer readable program code
to service the interrupt generated by a polling circuit in the chipset when the mode
is configured in one of the second and third modes, the polling circuit detecting
the insertion of the medium; and

computer readable program code to start a program on the medium when
the insertion is detected;

wherein the computer readable program code to service the interrupt comprises:

computer readable program code to check a status bit set by the polling circuit when the mode is configured in the second mode;

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computer readable program code to update a flag in a memory based on the status bit; and
computer readable program code to respond to a poll request by an operating system.

19. (original) The computer program product of claim 18 wherein the computer readable program code to respond comprises:

computer readable program code to read the flag from the memory.

20. (currently amended) The computer program product of claim ~~16~~ 18 wherein the computer readable program code to service the interrupt comprises:

computer readable program code to service the interrupt generated by the drive.

21. (withdrawn) A computer program product comprising:
a machine useable medium having computer program code embedded therein, the computer program product having:

computer readable program code to check a status bit in response to an interrupt generated by a polling circuit in a chipset, the polling circuit detecting insertion of a medium into a drive;

computer readable program code to update a flag in a memory based on the status bit; and

computer readable program code to respond to a poll request by an operating system.

22. (withdrawn) The computer program product of claim 21 wherein the computer readable program code to respond comprises:

computer readable program code to read the flag from the memory.

23. (withdrawn) The computer program product of claim 21 wherein the computer readable program code to check the status bit comprises:

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computer readable program code to check the status bit set by the polling circuit upon detecting the insertion of the medium.

24. (withdrawn) The computer program of claim 21 further comprising:
computer readable program code to load a program on the medium into a memory; and
computer readable program code to execute the program.

25. (canceled) A system comprising:
a processor;
a chipset coupled to the processor to control a drive; and
a memory coupled to the processor to store instruction code, the instruction code, when executed by the processor, causing the processor to:
configure a mode word in the chipset or in a configuration map stored in a non-volatile memory during boot-up,
detect insertion of a medium into the drive based on the mode word, and
start a program on the medium when the insertion is detected.

26. (canceled) The system of claim 25 wherein the instruction code causing the processor to configure the mode word causes the processor to:
configure the mode word in one of first, second, third, and fourth modes.

27. (currently amended) The system of claim ~~26~~ 30 wherein the instruction code causing the processor to detect insertion causes the processor to:
periodically poll the drive when the mode word is configured in the first mode.

28. (canceled) The system of claim 26 wherein the instruction code causing the processor to detect insertion causes the processor to:
service an interrupt indicating the insertion of the medium when the mode is configured in one of the second, third, and fourth modes.

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29. (canceled) The system of claim 28 wherein the instruction code causing the processor to service the interrupt causes the processor to:

service the interrupt generated by a polling circuit in the chipset when the mode is configured in one of the second and third modes, the polling circuit detecting the insertion of the medium.

30. (original) The A system of claim 29 comprising:

a processor;

a chipset coupled to the processor to control a drive; and

a memory coupled to the processor to store instruction code, the instruction code, when executed by the processor, causing the processor to;

configure a mode word in one of first, second, third, and fourth modes in the chipset or in a configuration map stored in a non-volatile memory during boot-up,

detect insertion of a medium into the drive based on the mode word, wherein the instruction code causing the processor to detect insertion causes the processor to service an interrupt indicating the insertion of the medium when the mode is configured in one of the second, third, and fourth modes, wherein the instruction code causing the processor to service the interrupt causes the processor to service the interrupt generated by a polling circuit in the chipset when the mode is configured in one of the second and third modes, the polling circuit detecting the insertion of the medium, and

start a program on the medium when the insertion is detected; and
wherein the instruction code causing the processor to service the interrupt causes the processor to:

check a status bit set by the polling circuit when the mode is configured in the second mode;

update a flag in a memory based on the status bit; and

respond to a poll request by an operating system.

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31. (withdrawn) A system comprising:
a processor;
a chipset coupled to the processor to control a drive, the chipset having a polling circuit to detect insertion of a medium into the drive; and
a memory coupled to the processor to store instruction code, the instruction code, when executed by the processor, causing the processor to:
check a status bit in response to an interrupt generated by the polling circuit when the insertion is detected,
update a flag in a memory based on the status bit, and
respond to a poll request by an operating system.
32. (withdrawn) The system of claim 31 wherein the instruction code causing the processor to respond causes the processor to:
read the flag from the memory.
33. (withdrawn) The system of claim 31 wherein the instruction code causing the processor to check the status bit causes the processor to:
check the status bit set by the polling circuit upon detecting the insertion of the medium.
34. (withdrawn) The system of claim 31 wherein the instruction code further causing the processor to:
load a program on the medium into a memory; and
execute the program.